

FIG. 1

110
212 AP1
203 PROCESSOR1
214 AP2
204 PROCESSOR2
218 AP4
216 AP3
205 PROCESSOR3
220
220
206
207
208

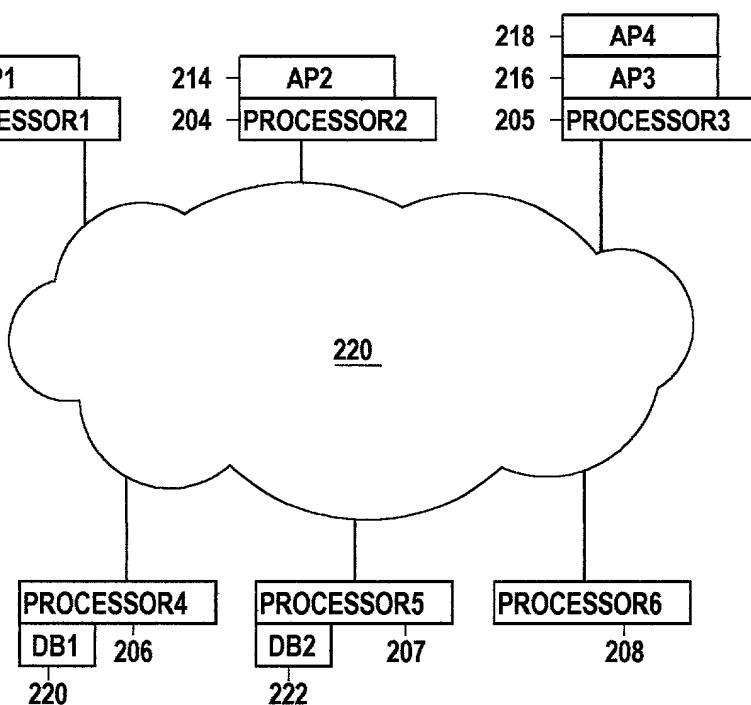


FIG. 2

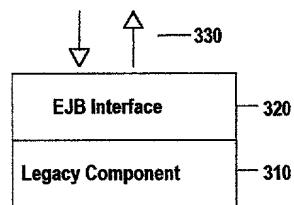


FIG. 3

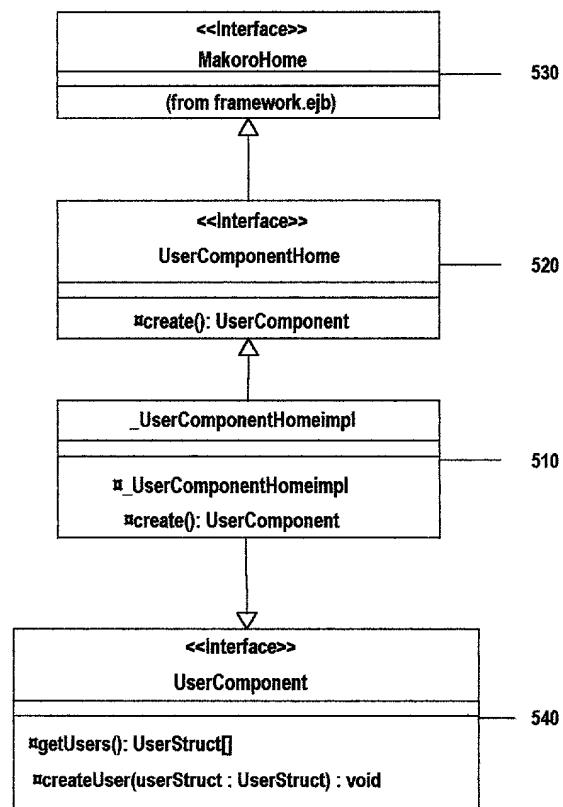
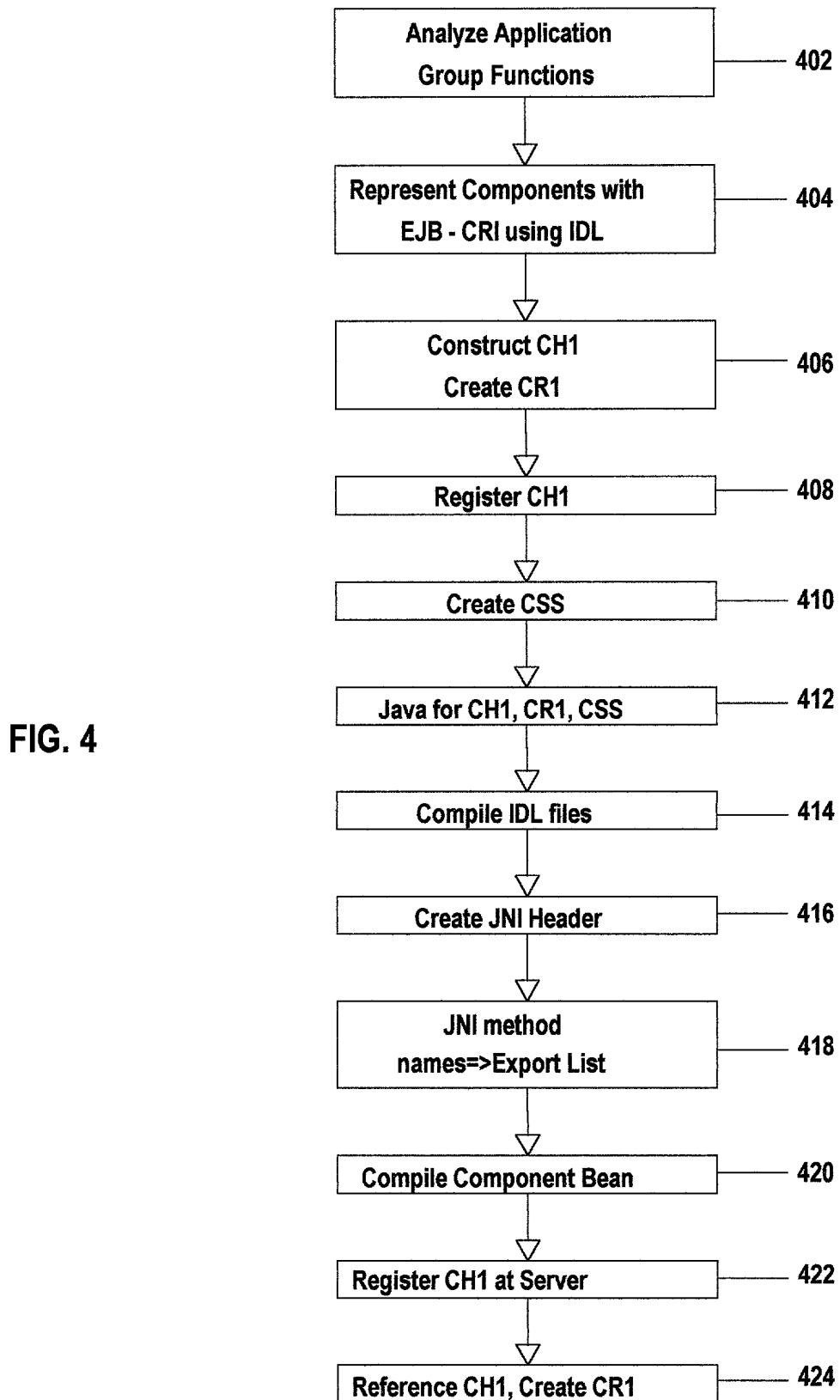


FIG. 5



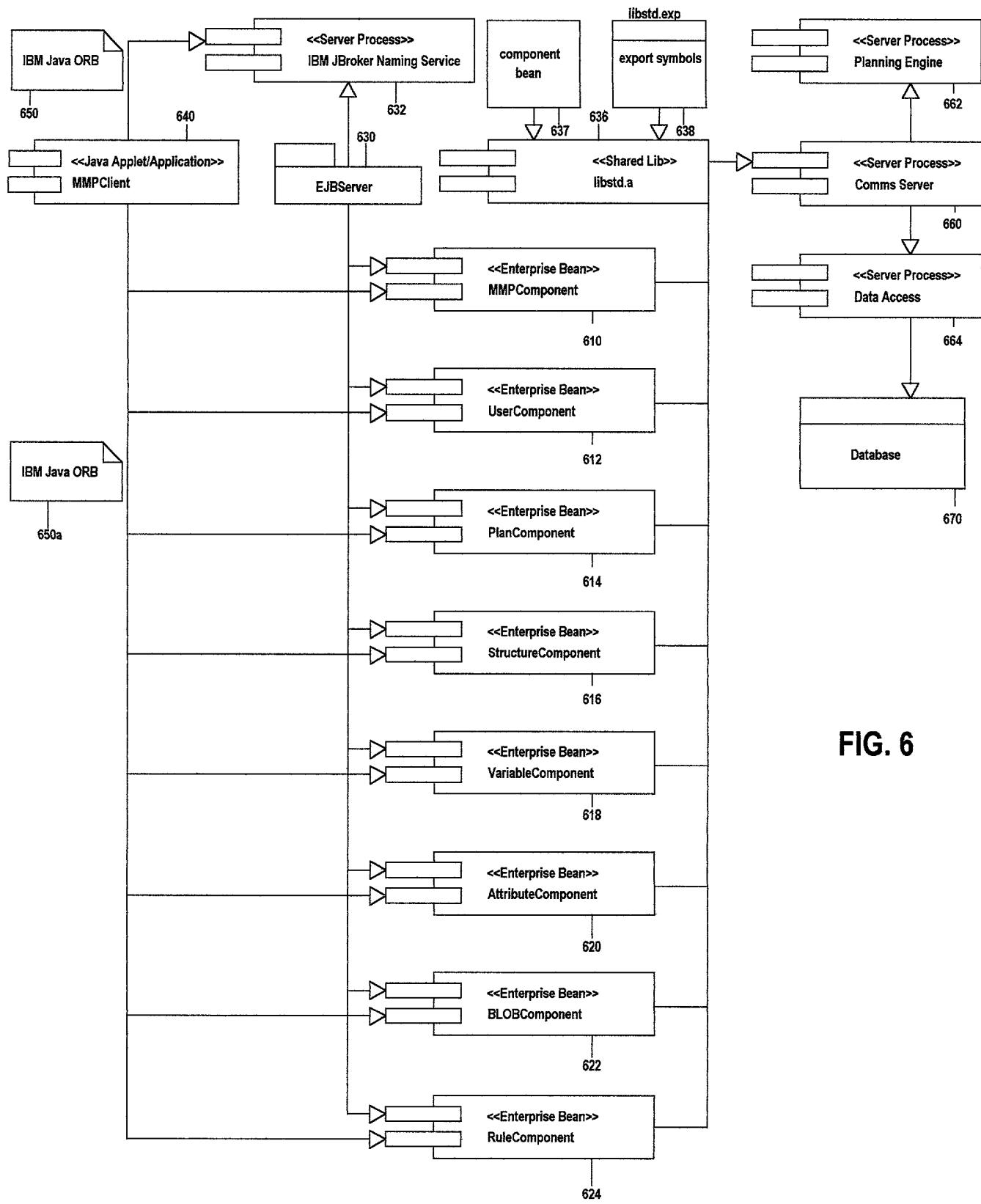


FIG. 6